

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S2	1	10/697865	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/12/09 19:13
S7	128	714/9,10,28,30,31,34,38.ccls. and (register processor near1 state) near3 (dump\$3 export\$3 copy\$3 stor\$3 read\$3 output\$3) same (second\$3 supplement\$3 auxiliary support\$3 servic\$3 alternat\$3 co) near3 (processor cpu microprocessor controller microcontroller "processing unit")	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/12/09 19:58
S8	2	09/410606	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/12/09 20:19
S9	151	"4907150" "6016400" "6820143" "7114042"	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/12/09 20:20
S10	539	(chang\$3 alter\$3 edit\$3 correct\$3 modify\$3) near3 registers same (second\$3 supplement\$3 auxiliary support\$3 servic\$3 alternat\$3 co) near3 (processor cpu microprocessor controller microcontroller "processing unit")	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/12/18 23:43
S11	426	(chang\$3 alter\$3 edit\$3 correct\$3 modify\$3) near3 registers same (second\$3 supplement\$3 auxiliary support\$3 servic\$3 alternat\$3 co) near3 (processor cpu microprocessor controller microcontroller "processing unit")	USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/12/09 23:19
S12	84	debug\$4 near3 ((cpu processor microprocessor) near1 state)	USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/12/10 02:56
S13	8	(("5398196") or ("5561761") or ("6711660") or ("6711700") or ("6839892") or ("6845470") or ("6961806") or ("7024581")).PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/12/10 01:06
S17	152	714/34.ccls. and (partner\$3 pair\$3 multiple second\$3 supplement\$3 auxiliary support\$3 servic\$3 alternat\$3 co) near3 (processor cpu microprocessor controller microcontroller "processing unit")	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/12/10 02:16

## EAST Search History

S25	75	714/38.ccls. and registers near2 (sav\$3 stor\$3 load\$3) same (chang\$3 alter\$3 edit\$3 correct\$3 modify\$3 fix\$3 repair\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/12/10 04:28
S27	70	714/9,10,28,30,31,34,38.ccls. and (validat\$3 check\$3 verify\$3 examin\$3 correct\$3 modify\$3 alter\$3 fix\$3) near3 (cpu processor microprocessor) near1 state	USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/12/10 03:32
S28	79	"5535331"	USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/12/10 03:33
S29	47	709/213,216.ccls. and (debug\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/12/10 04:33
S31	47	709/213,216.ccls. and (isolat\$3 secur\$3).near3 (processor controller cpu microcontroller microprocessor)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/12/10 04:33
S34	23	714/11.ccls. and (processor cpu "processing unit") adj2 (debug\$3 check\$3) adj3 (processor cpu "processing unit")	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/12/11 13:56
S36	109	714/38.ccls. and (microprocessor processor cpu) near1 state	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/12/13 12:13
S39	132	714/1-57.ccls. and (processor cpu microprocessor) adj4 (processor cpu microprocessor) near3 cache same (transfer\$3 transmit\$4 sav\$3 copy\$3 writ\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/12/18 23:45
S40	1	("5491787").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/12/18 23:52
S41	1	("5301328").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/12/19 01:56
S43	60	(reserv\$3 adj memory).ab. and (reserv\$3 adj memory).bsum.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/12/19 02:32
S44	2	10/419524	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/12/19 02:32